

**IN THE SPECIFICATION:**

Please amend the Specification as follows:

Please replace the paragraph beginning at line 20 of page 1 with the following amended paragraph:

Among the MOS type solid-state imaging devices, those capable of ~~pickuppig~~ picking up two-dimensional light images have conventionally employed a system in which a number of photodetectors two-dimensionally arranged on a semiconductor chip are provided with their discrete amplifiers and A/D converters, respective current signals outputted from the individual photodetectors are amplified by the amplifiers to yield voltage signals, the resulting voltage signals are converted into digital signals by the A/D converters, and thus obtained digital signals are outputted. In recent years, however, there have been proposed attempts in which, while amplifiers and A/D converters are mounted on the same chip with a photodetector array, the circuit configuration system is altered, in order to reduce the size of the device while taking advantage of being a MOS type.